

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

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Title of Invention	INTEGRATED CIRCUIT YIELD ENHANCEMENT USING VORONOI DIAGRAMS																																						
<p>Application Number : Confirmation Number: First Named Applicant: Robert Allen Attorney Docket Number: BUR920030135US1 Art Unit: Examiner: Search string: (6178539 or 6247853 or 6317859).pn</p>																																							
<h3>US Patent Documents</h3> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>6178539</td><td>2001-01-23</td><td>Papadopoulou et al.</td><td></td><td></td><td></td></tr><tr><td></td><td>2</td><td>6247853</td><td>2001-06-19</td><td>Papadopoulou et al.</td><td></td><td></td><td></td></tr><tr><td></td><td>3</td><td>6317859</td><td>2001-11-13</td><td>Papadopoulou et al.</td><td></td><td></td><td></td></tr></tbody></table>								init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	6178539	2001-01-23	Papadopoulou et al.					2	6247853	2001-06-19	Papadopoulou et al.					3	6317859	2001-11-13	Papadopoulou et al.			
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